BOOSTER ACCELERATOR LLRF SYSTEM BLOCK DIAGRAM 11/10/2016
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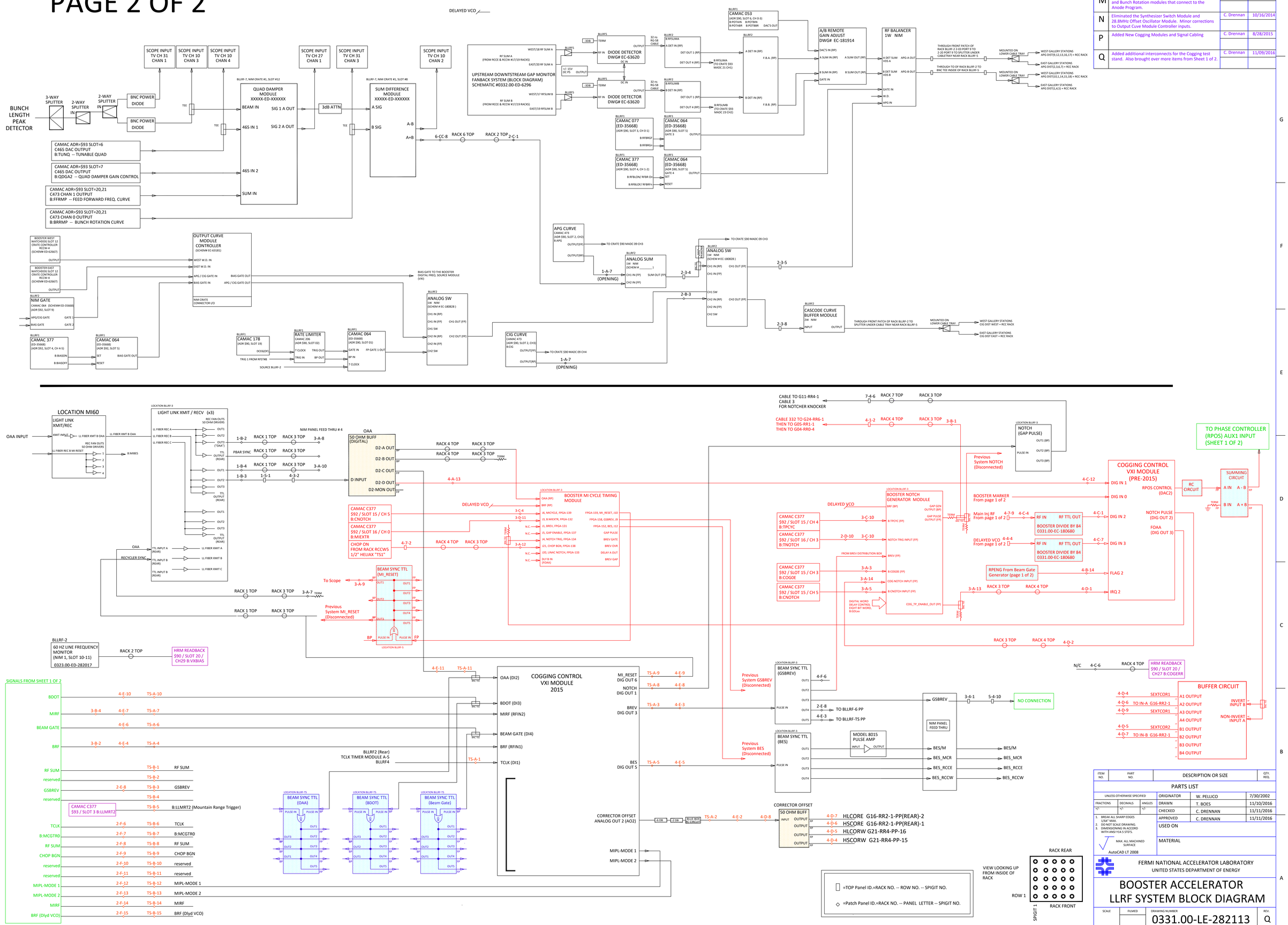
REV.	DESCRIPTION	DRAWN / APPD.	DATE / DATE
K			
L	Updated Block Diagram to include corrections and new HRM Readback Channels in Crate $90 and added Cogging VXI Module block to this page	C. Drennan	9/26/2013
M	Updated Block Diagram to include the Quad Damper and Bunch Rotation modules that connect to the Anode Program.	C. Drennan	2/20/2014
N	Eliminated the Synthesizer Switch Module and 28.8MHz Offset Oscillator Module. Minor corrections to Output Cuve Module Controller inputs.	C. Drennan	10/16/2014
P	Added New Cogging Modules and Signal Cabling	C. Drennan	8/28/2015
Q	Added additional interconnects for the Cogging test stand. Also brought over more items from Sheet 1 of 2.	C. Drennan	11/09/2016



ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY. REQ.
PARTS LIST			

UNLESS OTHERWISE SPECIFIED				
FRACTIONS	DECIMALS	ANGLES	ORIGINATOR	W. PELLICO	7/30/2002
+/-	+/-	+/-	DRAWN	T. BOES	11/10/2016
			CHECKED	C. DRENNAN	11/11/2016
			APPROVED	C. DRENNAN	11/11/2016

1. BREAK ALL SHARP EDGES 1/64" MAX.
2. DO NOT SCALE DRAWING.
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD'S.

MAX. ALL MACHINED SURFACE

AutoCAD LT 2008

USED ON

MATERIAL

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

BOOSTER ACCELERATOR
LLRF SYSTEM BLOCK DIAGRAM

SCALE | FILMED | DRAWING NUMBER 0331.00-LE-282113 | REV. Q